

# Sublogarithmic uniform Boolean Proof Nets

DICE 2011 Second Workshop on Developments in Implicit  
Computational Complexity

Clément Aubert

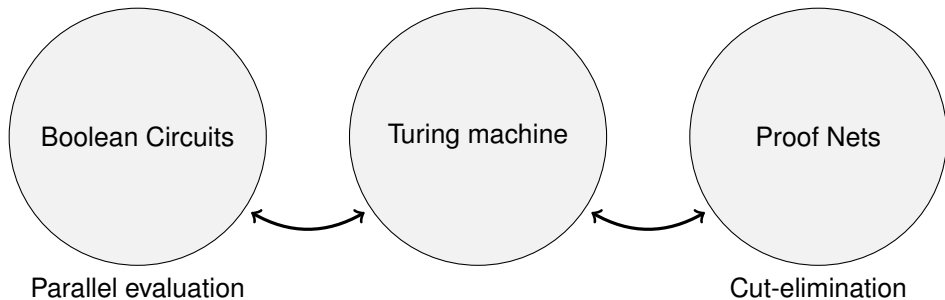
`aubert@lipn.univ-paris13.fr`



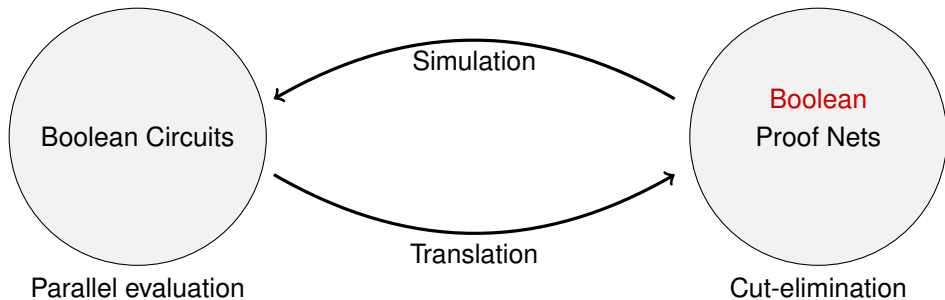
Institut Galilée - Université Paris-Nord  
99, avenue Jean-Baptiste Clément  
93430 Villetaneuse

2nd-3rd April 2011, Saarbrücken

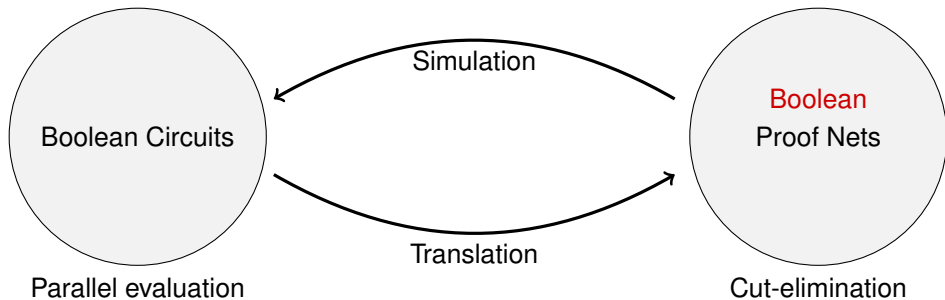
## Two models of parallel computation



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### Implicit complexity framework:

- Size and depth
- Resources needed by the reductions
- Uniformity

## Previous works

- [Terui, 2004] did not take into account **uniformity**.
- [Mogbil and Rahli, 2007] did, but translation was in  $L$ .

# Previous works and new results

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- [Terui, 2004] did not take into account **uniformity**.
- [Mogbil and Rahli, 2007] did, but translation was in  $L$ .

## Results

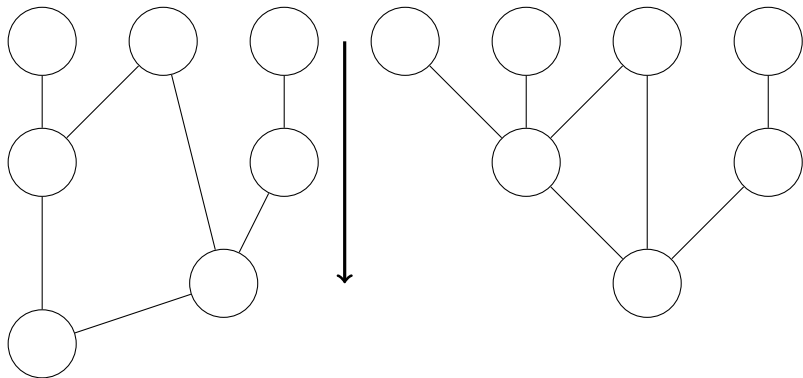
- ***Proof Circuits** lighten the size and translate all kind of Boolean Circuits.*
- *Translation in  $AC^0$  concerning uniform families.*
- *Proof Nets compute functions under  $L$ .*



# *Map*

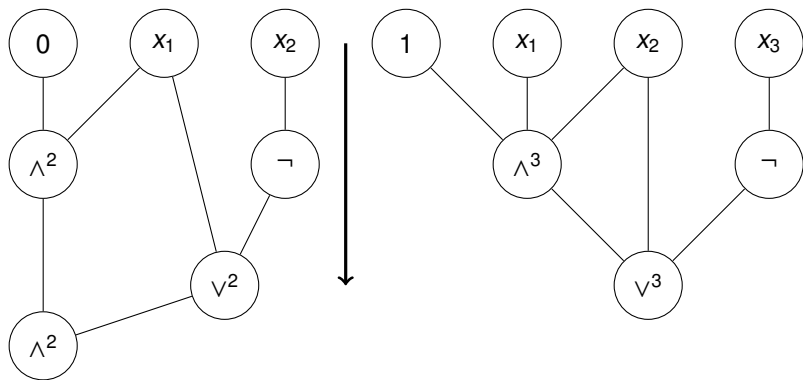
- **First definitions: Boolean Circuits and Boolean Proof Nets**
- Simulation of Boolean Proof Nets by Boolean Circuits
- Examples of translation and composition
- Proof Circuits and translation

## Examples (Two Boolean Circuits)





## Examples (Two Boolean Circuits)



## Definition (Basis)

$$\mathcal{B}_0 = \{\neg, \wedge^2, \vee^2\}$$

$$\mathcal{B}_1 = \{\neg, (\wedge^n)_{n \geq 2}, (\vee^n)_{n \geq 2}\}.$$

## Definition ( $NC^i$ resp. $AC^i$ )

For  $i \in \mathbb{N}$ , set of boolean functions computable by a uniform family of Boolean Circuits  $C = (C_n)$  where for all  $C_n$

- its depth is  $O(\log^i n)$ ,
- its size is polynomial in  $n$ ,
- its gate are labeled with functions of  $\mathfrak{B}_0$  resp.  $\mathfrak{B}_1$ .

$$NC = \bigcup_{i \in \mathbb{N}} NC^i = \bigcup_{i \in \mathbb{N}} AC^i = AC$$

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## Theorems

$$AC^0 \subsetneq NC^1 \subseteq L \subseteq NL \subseteq AC^1 \subseteq NC^1 \subseteq AC^2 \subseteq \dots$$

$$AC^0(\text{UstCONN}_2) \subseteq AC^1 \supseteq L$$

Definition (Rules of **MLLu**)

$$\begin{array}{c}
 \frac{}{\vdash A, A^\perp} \text{ax.} \qquad \frac{\vdash \Gamma_1, A_1 \quad \dots \quad \vdash \Gamma_n, A_n}{\vdash \Gamma_1, \dots, \Gamma_n, \otimes^n(A_1, \dots, A_n)} \otimes^n \\
 \\
 \frac{\vdash \Gamma, A \quad \vdash \Delta, A^\perp}{\vdash \Gamma, \Delta} \text{cut} \qquad \frac{\vdash \Gamma, A_n, \dots, A_1}{\vdash \Gamma, \wp^n(A_n, \dots, A_1)} \wp^n
 \end{array}$$

## Definition (Boolean Type, [Terui, 2004])

$$\frac{\frac{\frac{}{\vdash \alpha^\perp, \alpha} \text{ax.}}{\vdash \alpha^\perp, \alpha^\perp, \alpha \otimes \alpha} \otimes^2}{\vdash \wp^3(\alpha^\perp, \alpha^\perp, \alpha \otimes \alpha)} \wp^3 \text{ax.}$$

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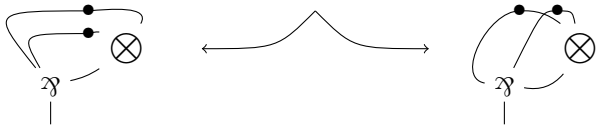
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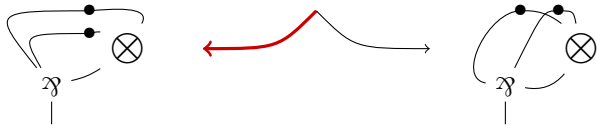
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## Definition (Boolean Type, [Terui, 2004])

$$\frac{}{\vdash p : \alpha^\perp, p : \alpha \triangleright ax_p} ax. \quad \frac{}{\vdash q : \alpha^\perp, q : \alpha \triangleright ax_q} ax.$$

$$\frac{\vdash p : \alpha^\perp, q : \alpha^\perp, r : \alpha \otimes \alpha \triangleright tensor_r^{p,q}(ax_p, ax_q)}{\vdash s : \wp^3(\alpha^\perp, \alpha^\perp, \alpha \otimes \alpha) \triangleright par_s^{q,p,r}(tensor_r^{p,q}(ax_p, ax_q))} \wp^3$$

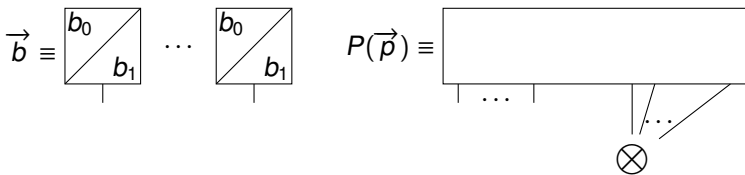


## Definition (Boolean Proof Nets, [Terui, 2004])

A Boolean Proof Net  $P(\vec{p})$  with  $n$  inputs is a Proof Net of type

$$\vdash p_1 : \mathbf{B}^\perp[A_1], \dots, p_n : \mathbf{B}^\perp[A_n], s : \otimes^{1+m}(\mathbf{B}[A], C_1, \dots, C_m)$$

## Definition ( $P(\vec{b})$ )

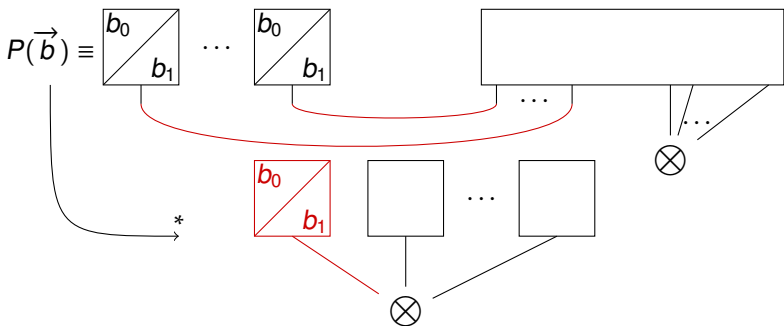


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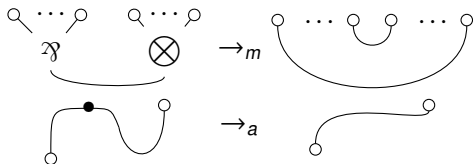
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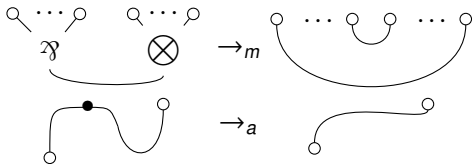
Definition ( $\rightarrow_m$  and  $\rightarrow_a$ )



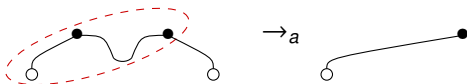
Remark (Critical pair)



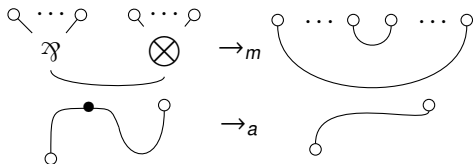
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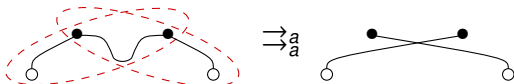
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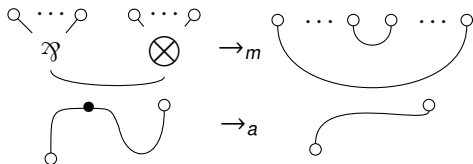
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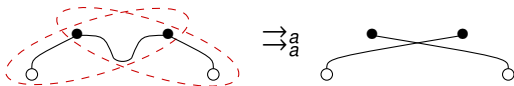
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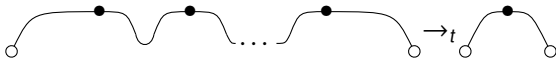
Definition ( $\rightarrow_m$  and  $\rightarrow_a$ )



Remark (Critical pair)



Definition ( $t$ -reduction)





# *Map*

- First definitions: Boolean Circuits and Boolean Proof Nets
- **Simulation of Boolean Proof Nets by Boolean Circuits**
- Examples of translation and composition
- Proof Circuits and translation

# Simulation of $\Rightarrow$ by Boolean Circuits

## Theorem ([Terui, 2004])

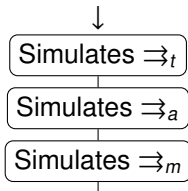
*Every Proof-Net  $P$  normalizes in at most  $3 \times d(P)$  steps of parallel reduction ( $\Rightarrow_t, \Rightarrow_a, \Rightarrow_m$ ).*

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Description of a Boolean Proof Net  $P_n$

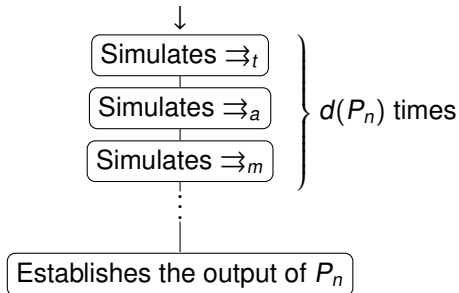


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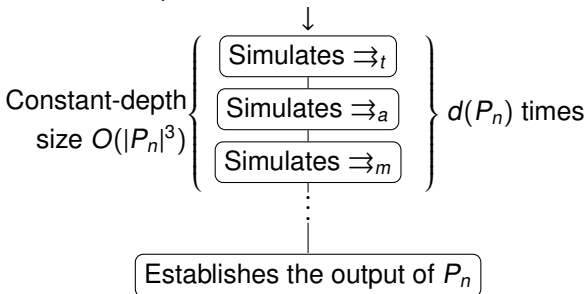


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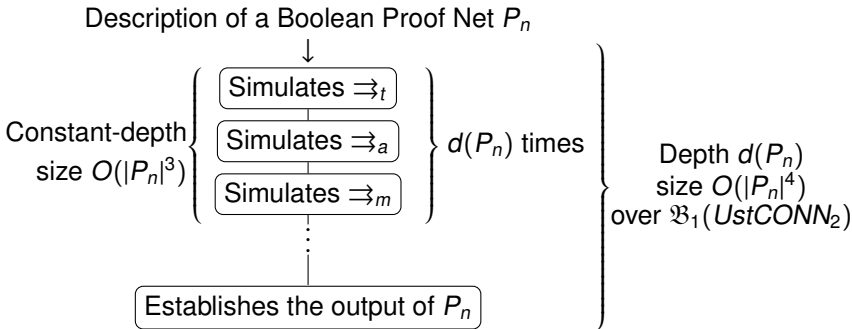
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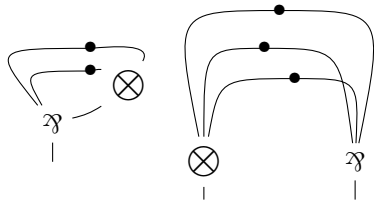




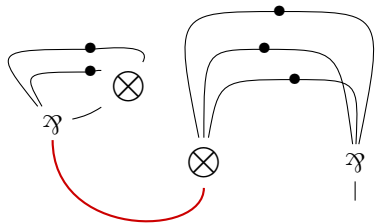
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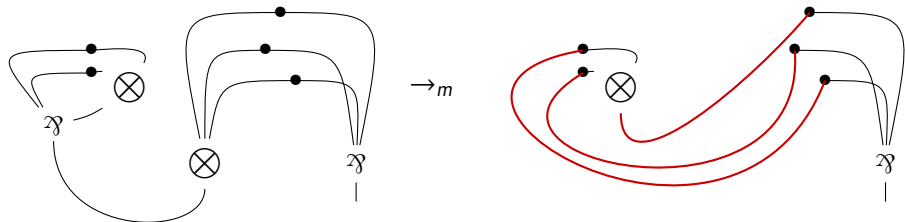
# First example: negation



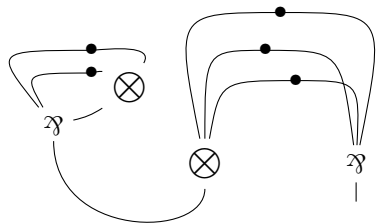
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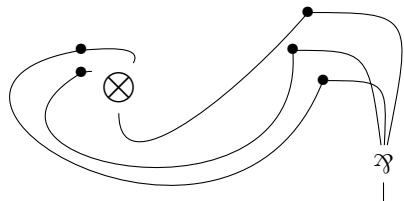
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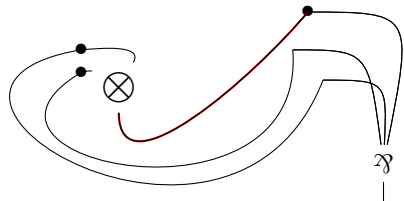
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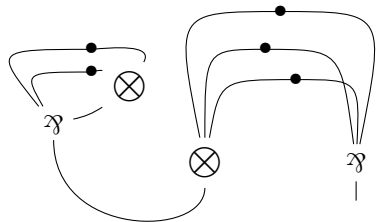
$\rightarrow m$



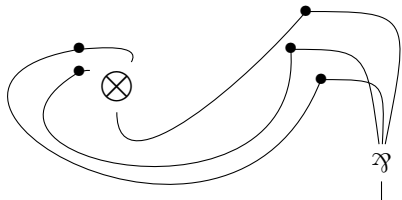
$\Downarrow t$



# First example: negation



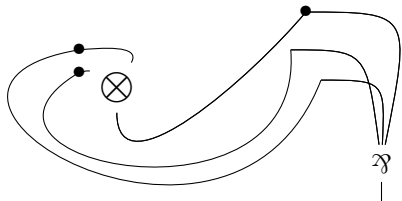
$\rightarrow m$



$\downarrow ev$



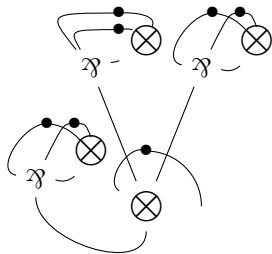
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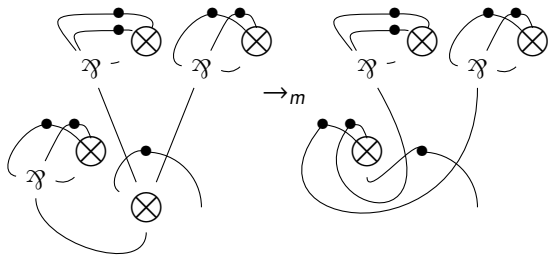
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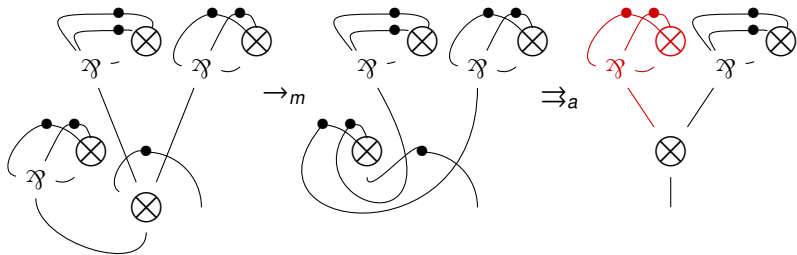
## Second example: conditional



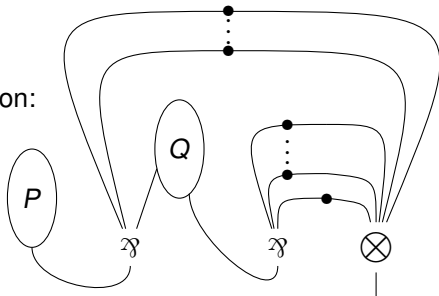
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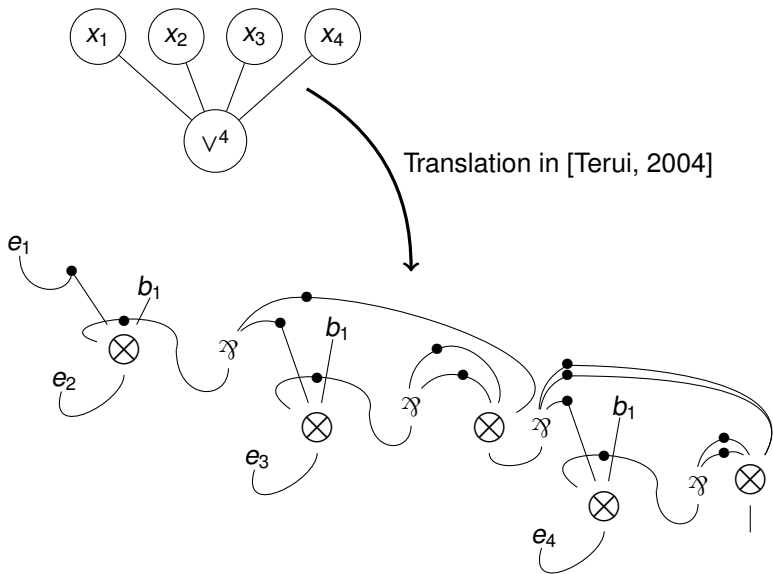


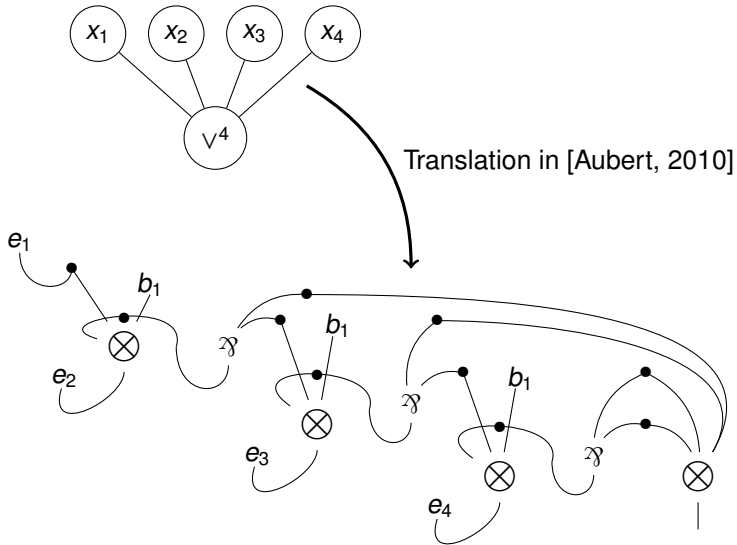
## Second example: conditional



Composition:







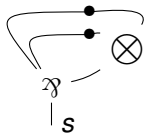


# *Map*

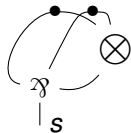
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# Pieces (1/4)

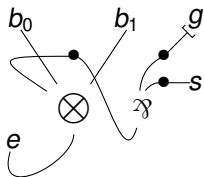
$b_0 \equiv$



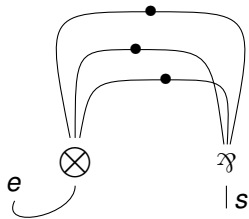
$b_1 \equiv$



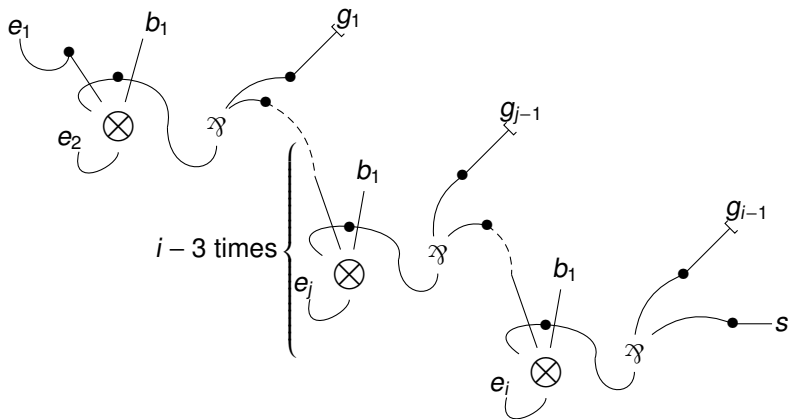
$COND \equiv$



$NEG \equiv$

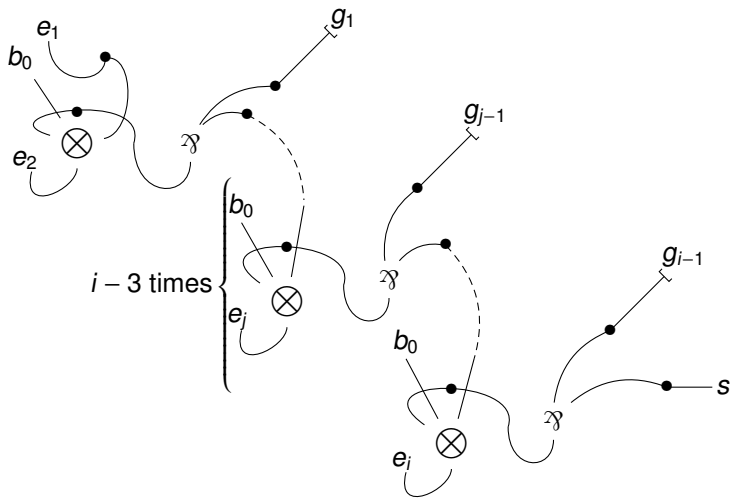


$DISJ^i \equiv$

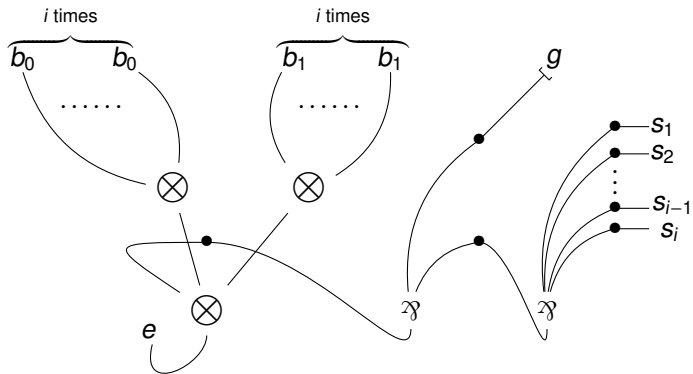




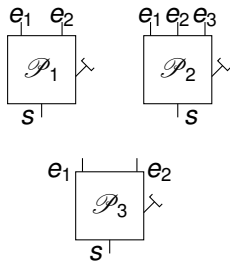
$CONJ^i \equiv$



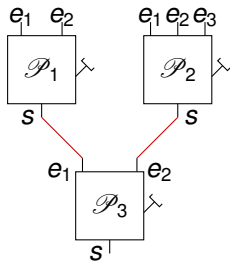
$DUPL^i \equiv$



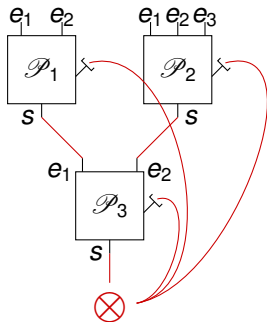
## Definition (Proof Circuits)



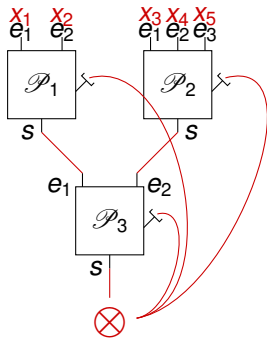
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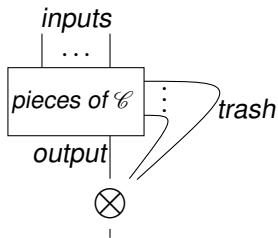
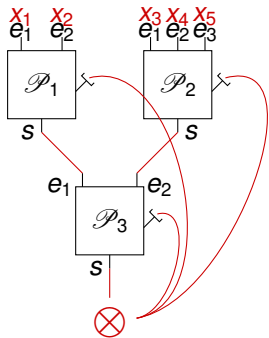
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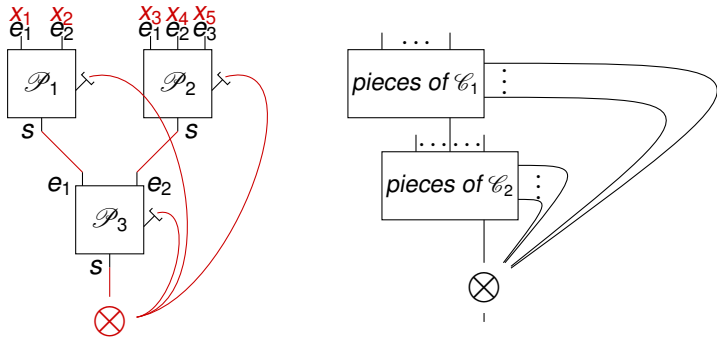
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## Theorem ([Aubert, 2010])

*Every Proof Circuits is a Boolean Proof Net.*



## Definition ( $CCP^i$ [Aubert, 2010])

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$$CCP = \bigcup_{i \in \mathbb{N}} CCP^i$$

## Theorem (Simulation)

For all  $i \in \mathbb{N}$ ,

$$CCP^i \subseteq AC^i(UstCONN_2)$$

## Definition (Translation from $AC^i$ to $CCP^i$ )

*Input:* Description of a uniform family of Boolean Circuits  $C = (C_n)$  in  $AC^i$ .

*Output:* Description of a family of Proof Circuits  $P = (P_n)$  in  $CCP^i$  so that for all  $k$ , for all  $\vec{b}$ ,  $P_k(\vec{b}) \rightarrow_{ev} b_j$  iff  $C_k(\vec{b})$  evaluates to  $j$ .

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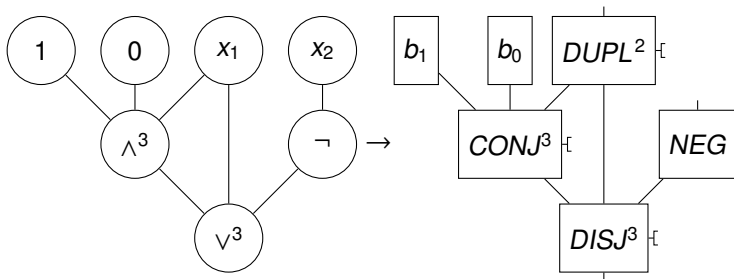
## Theorem

*Translation from  $AC^i$  to  $CCP^i$  belongs to  $AC^0$ .*

# Complexity of the translation

## Theorem

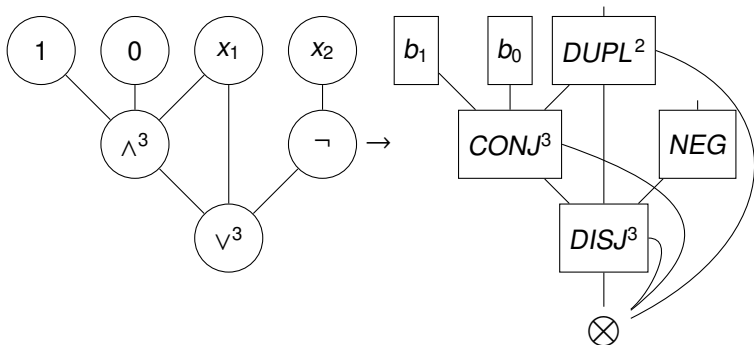
*Translation from  $AC^i$  to  $CCP^i$  belongs to  $AC^0$ .*



# Complexity of the translation

## Theorem

*Translation from  $AC^i$  to  $CCP^i$  belongs to  $AC^0$ .*



## Lemmas ([Aubert, 2010])

- *For  $n, m \in \mathbb{N}$ , a gate of fan-in  $n$  and fan-out  $m$  labeled by a function of  $\mathfrak{B}_1$  is translated with a piece of size  $9(n + m)$ .*

## Lemmas ([Aubert, 2010])

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## Future work:

- $UstConn_2 \in L$ , is there a lighter function?
- Link between Proof Nets and Alternating Turing Machines?



Aubert, C. (2010).

Réseaux de preuves booléens sous-logarithmiques.  
Mémoire de M2 L.M.F.I., Paris VII, L.I.P.N.



Mogbil, V. and Rahli, V. (2007).

Uniform circuits, & Boolean proof nets.  
In *Proceedings of LFCS'07*, pages 401–421. Springer.



Terui, K. (2004).

Proof Nets and Boolean Circuits.  
In *Proceedings of LICS'04*, pages 182–191.

Thanks!